

Test 2-EE 5340/001&051 (print last name) **Solution** (print first name) _____

Thursday, October 29, 2003, 8:00 AM, 206 Activities Building

75 minutes allowed (last four digits of your student #) _____ (e-mail if new) _____

Seat Number _____ Enrolled in (circle one) 8:00 AM or 9:30 AM class

Instructions:

1. Do your own work. DO NOT REMOVE THE STAPLE ON THIS EXAM.
2. You may use either a legal copy of the text OR reference text. You may NOT pass a book or note sheet to another student. You may NOT use class notes. Do not use previously solved problems.
3. Calculator allowed. You may NOT share a calculator with another student.
4. Use values given on this cover sheet. If a value is not given, explicitly state definitions and assumptions that you use.
5. Where possible, calculate parameters rather than read them from a graph.
6. Do all work in the spaces provided on this exam paper. If you write on the back of a sheet, make the notation "PTO" in your solution in order to assure that material written on the back of the page is evaluated for a grade. AN EXTRA BLANK SHEET IS ATTACHED AT THE BACK OF THE EXAM.
7. Show all calculations, making numerical substitutions and giving numerical results where possible.
8. Write answers in space given.
9. Unless stated otherwise,

$$T = 300\text{K},$$

$$V_t = 25.843 \text{ mV (to agree with M\&K } k \text{ and } q \text{ values)}$$

10. Unless otherwise stated, the material is silicon (300K) with

$$n_i = 1.45\text{E}10 \text{ cm}^{-3}$$

$$N_c = 2.8\text{E}19 \text{ cm}^{-3}$$

$$q\chi_{\text{Si}} = 4.05 \text{ eV}$$

$$E_{g,\text{Si}} = 1.124 \text{ eV.}$$

$$N_v = 1.04\text{E}19 \text{ cm}^{-3}$$

11. For the work function of poly silicon, use

$$\phi_{n+} = \chi_{\text{Si}} = 4.05 \text{ V}$$

$$\phi_{p+} = \chi_{\text{Si}} + E_{g,\text{Si}}/q = 5.174 \text{ V.}$$

12. For minority carrier (either electrons or holes) lifetime in silicon, use the relationship

$$\tau_{\text{min}} = (4.5\text{E}-5 \text{ sec}) / (1 + N_i/1\text{E}17 + (N_i/5\text{E}17)^2),$$

where N_i = the total impurity concentration in cm^{-3}

13. For holes in silicon doped primarily with boron, assume

$$\mu_p = \{470.5 \div [1 + (N_i \div 2.23\text{E}17)^{0.719}]\} + 44.9, \text{ in cm}^2/\text{V-sec.}$$

14. For electrons in silicon doped primarily with phosphorous, assume

$$\mu_n = \{1414 \div [1 + (N_i \div 9.2\text{E}16)^{0.711}]\} + 68.5, \text{ in cm}^2/\text{V-sec.}$$

15. For electrons in silicon doped primarily with arsenic, assume

$$\mu_n = \{1417 \div [1 + (N_i \div 9.68\text{E}16)^{0.68}]\} + 52.2, \text{ in cm}^2/\text{V-sec.}$$

(In 12 through 16, N_i = the total impurity concentration in n- or p-type material, compensated or not).

16. Metal gate work functions should be assumed to be

$$\phi_{M,\text{Al}} = 4.1 \text{ V for aluminum,}$$

$$\phi_{M,\text{Pt}} = 5.3 \text{ V for platinum,}$$

$$\phi_{M,\text{Au}} = 4.75 \text{ V for gold}$$

17. The electron affinity of SiO_2 is

$$\chi_{\text{SiO}_2} = 1.00 \text{ V.}$$

18. Planck constant

$$h = 6.625\text{E}-34 \text{ J-s} = 4.135\text{E}-15 \text{ eV-s, (1 eV} = 1.602\text{E}-19 \text{ Joule).}$$

19. free electron mass

$$m_0 = 9.11\text{E}-28 \text{ g.}$$

20. Boltzmann constant,

$$k = 1.38\text{E}-23 \text{ J/K}$$

21. Electron charge,

$$q = 1.602\text{E}-19 \text{ Coulomb}$$

22. Permittivity of free space,

$$\epsilon_0 = 8.854\text{E}-14 \text{ Fd/cm}$$

23. Relative permittivity of silicon,

$$\epsilon_r = 11.7$$

24. Relative permittivity of silicon dioxide, $\epsilon_{\text{rOx}} = 3.9$

25. The breakdown voltage of an abrupt (step) junction (asymmetrical or one-sided) diode with doping on the lightly doped side of N_B is $V_B = 60(E_g/1.1)^{3/2} (10^{16}/N_B)^{3/4} \text{ V}$. The critical field for breakdown is modeled as $E_{\text{crit}} = (120\text{V}\cdot\text{q}N_B/(\epsilon_r\epsilon_0))^{1/2} \cdot (E_g/1.1)^{3/4} \cdot (10^{16}/N_B)^{3/8}$

26. Each part is worth [x] points, as given in the problem.

1 A JFET is fabricated using a p⁺ gate ($N_a = 8E18 \text{ cm}^{-3}$) and an n-channel ($N_d = 4E15 \text{ cm}^{-3}$, phosphorous). The available channel thickness (from the p⁺n junction to the channel to substrate depletion region (i.e., $x_d + x_w \sim t$ in Figure 4.19 in M&K 3rd edition) is 0.8 μm . The channel length is 5 μm and the channel width is 9 μm .

a Calculate $\phi_i - V_T$ (where V_T or V_P is the turn-off voltage, and $\phi_i = V_{bi}$) for this device.

Assume $W \sim xn$ since $N_a \gg N_d$

$$\text{PHI } i - V_T = q \cdot 4e15 \cdot (0.8e-5)^2 / (2 \cdot \text{epsSi} \cdot \text{eps0})$$

$$\text{PHI } i - V_T = 1.979 \text{ V}$$

a [6] $\phi_i - V_T = \underline{\hspace{2cm}}$ V.

b Calculate G_0 (the open channel conductance) for this device.

$$\text{SIGMAN} = n \cdot q \cdot \text{Mun} = 4e15 \cdot q \cdot ((1414 / (1 + (4e15 / 9.2E16)^{0.711})) + 68.5)$$

$$\text{SIGMAN} = 0.8620 / (\text{Ohm-cm})$$

$$G_0 = \text{SIGMAN} \cdot W \cdot t / L = \text{SIGMAN} \cdot 0.8e-4 \cdot 9 / 5 = 0.1241 \text{ mA/V}$$

b [6] $G_0 = \underline{\hspace{2cm}}$ mA/V.

c Calculate ϕ_i for this device.

$$\text{PHI } i = V_t \cdot \ln(N_a \cdot N_d / n_i^2) = V_t \cdot \ln(8e18 \cdot 4e15 / n_i^2)$$

$$\text{PHI } i = 844 \text{ mV}$$

c [6] $\phi_i = \underline{\hspace{2cm}}$ mV.

d Calculate $I_{Dsat0} = I_{Dsat}$ (for $V_G = 0$) for this device.

$$I_{Dsat0} = G_0 \cdot ((\text{PHI } i - V_T) / 3 - \text{PHI } i \cdot (1 - (2/3) \cdot (\text{PHI } i / (\text{PHI } i - V_T))^{0.5}))$$

$$I_{Dsat0} = 2.27E-02 \text{ mA}$$

d [7] $I_{Dsat0} = \underline{\hspace{2cm}}$ mA.

2 An n-type wafer with $\phi_n = 234$ mV has a planar diode constructed with $N_a = 3E18$ cm⁻³ in the p-region.
 a Find the breakdown voltage (BV) for this diode assuming $BV > \phi_i = V_{bi}$.

$$N_d = n_i \cdot e^{(\phi_n / V_t)} = n_i \cdot \exp(234 / V_t) = 1.241E+14 \text{ cm}^{-3}$$

This is much less than N_a , so $N_d = N_B$ is the lightly doped side.

$$V_B = 60 \cdot ((E_{gSi} / 1.1)^{3/2}) \cdot (1e16 / N_B)^{3/4} \text{ V}$$

$$V_B = 60 \cdot ((E_{gSi} / 1.1)^{3/2}) \cdot (1e16 / 1.241e14)^{3/4} \text{ V}$$

$$V_B = 1.67E+03 \text{ V}$$

a [6] $V_{BV} = \underline{\hspace{2cm}}$ V.

b What is the depletion width at breakdown for this device?

$$W = \sqrt{2 \cdot \epsilon_0 \cdot \epsilon_{Si} (\phi_i - V_a) / (q \cdot (N_a \cdot N_d / (N_a + N_d)))}$$

At breakdown, $V_B = \phi_i - V_a = 1.67E3$ V

$$W = \sqrt{2 \cdot \epsilon_0 \cdot \epsilon_{Si} (1.667E3) / (q \cdot (3E18 \cdot 1.241E14 / (3E18 + 1.241E14)))}$$

At breakdown, $W = 1.318E-02$ cm

b [7] $W_{\text{breakdown}} = \underline{\hspace{2cm}}$ cm.

3 A silicon sample with $N_d = 7E15$ cm⁻³ has traps at $E_T = E_{fi}$ with a concentration of $8E14$ cm⁻³. The capture cross sections for the traps are $\sigma_n = \sigma_p = 3E-15$ cm². Let $v_{th} = 1.5E7$ cm/s.

a Calculate the minority carrier generation rate in a region which is depleted of mobile carriers.

For $E_T = E_{fi}$, the recombination rate becomes

$$G = -U = -(np - n_i^2) / ((p+n+2 \cdot n_i) \cdot \tau_{AU0})$$

$$\tau_{AU0} = (N_T \cdot \sigma \cdot v_{th})^{-1}$$

$$\tau_{AU0} = (8E14 \cdot 3E-15 \cdot 1.5E7)^{-1} = 2.778E-08 \text{ sec}$$

$n = p = 0$, so $G = n_i / (2 \cdot \tau_{AU0}) = 2.610E+17 \text{ cm}^{-3} \text{ sec}^{-1}$

a [6] $U = \underline{\hspace{2cm}}$ cm⁻³-sec⁻¹.

b Calculate the minority carrier generation rate in a region where the majority carrier concentration is essentially at the equilibrium value, but the minority carrier concentration is essentially zero.

$n = N_d = 7E15$, $p = 0$, so

$$G = -U = -(np - n_i^2) / ((p+n+2 \cdot n_i) \cdot \tau_{AU0}) = -(-n_i^2) / ((7E15+2 \cdot n_i) \cdot \tau_{AU0})$$

$$G = 1.081E+12 \text{ cm}^{-3} \text{ sec}^{-1}$$

b [6] $U = \underline{\hspace{2cm}}$ cm⁻³-sec⁻¹.

4 An ideal, planar p⁺n diode has $N_a = 8E18 \text{ cm}^{-3}$, and $N_d = 6E16 \text{ cm}^{-3}$ with no compensation in either region. The n-region measures $5E-4 \text{ cm}$ from junction to contact.

a Calculate the minority carrier diffusion coefficient for the n-region.

In the n-region, the minority carriers are holes, and $N_i = N_d = 6E16$,

$$D_{min} = \mu_p \cdot V_t$$

$$\mu_p = \mu_{pB} = (470.5 / (1 + (6E16 / 2.23E17)^{0.719})) + 44.9$$

$$D_{min} = 9.914 \text{ cm}^2/\text{sec}$$

a [6] $D_{min} = \underline{\hspace{2cm}} \text{ cm}^2/\text{sec}$.

b Calculate the minority carrier diffusion length for the n-region.

$$L_{min} = \sqrt{D_{min} \cdot \tau_{Umin}} = \sqrt{D_{min} \cdot ((45E-6) / (1 + 6E16 / 1E17 + (6E16 / 5E17)^2))}$$

$$L_{min} = 1.662E-02 \text{ cm}$$

b [6] $L_{min} = \underline{\hspace{2cm}} \text{ cm}$.

c Calculate the saturation current density for the lightly doped side.

Since $L_{min} \gg$ the dimension of the n-region, let $L_{min} \cdot \tanh(W_n / L_{min}) = W_n$

$W_n = 5E-4 \text{ cm} - x_n$, where x_n is the DR on the n-side $\sim W$, the total D.R.

$$\phi_{hi} = V_t \cdot \ln(N_a \cdot N_d / n_i^2) = V_t \cdot \ln(8E18 \cdot 6E16 / n_i^2) = 913.9 \text{ mV}$$

$$\text{At } V_a = 0, W \sim \sqrt{2 \cdot \epsilon_0 \cdot \epsilon_{Si} \cdot \phi_{hi} / (q \cdot 6E16)} = 1.404E-05 \text{ cm}$$

$$W_n = 5E-4 - 1.404E-5 = 4.860E-04 \text{ cm}$$

$$J_s = q \cdot n_i^2 \cdot D_{min} / (N_d \cdot 4.86E-4) = 1.145E-11 \text{ A/cm}^2$$

c [7] $J_s = \underline{\hspace{2cm}} \text{ A/cm}^2$.

d Calculate the small-signal resistance for the diode at $V_a = 30 V_t$. Assume the contribution due to the heavily doped side is negligible. Assume the diode junction area is $1e-4 \text{ cm}^2$.

$$I_d = J_s \cdot A \cdot \exp(V_a / V_t) = 1.145E-11 \cdot 1E-4 \cdot (\exp(30) - 1) = 1.224E-02 \text{ A}$$

$$r_d = V_t / I_d = 2.112 \text{ Ohm}$$

d [6] $r_d = \underline{\hspace{2cm}} \text{ Ohm}$.

5 An ideal prototype BJT structure has $N_{dE} = 1E19 \text{ cm}^{-3}$, $N_{aB} = 5E16 \text{ cm}^{-3}$, $N_{dC} = 1E15 \text{ cm}^{-3}$, with no compensation in any region. Assume the charge neutral base width is $0.8E-4 \text{ cm}$. The emitter area and collector areas are $100E-8 \text{ cm}^2$. Assume phosphorous parameters when electron minority carrier values are needed.

a Calculate the emitter efficiency for this BJT. You may assume D_{\min} for the emitter is $1.5 \text{ cm}^2/\text{sec}$ and that the emitter charge neutral region width is the same as the base.

$$D_{b\min} = \mu_n(\text{Phos}) \cdot V_t = \left(\frac{1414}{1 + (5E16/9.2E16)^{0.711}} \right) + 68.5 \cdot V_t$$

$$= 23.94 \text{ cm}^2/\text{sec}$$

$$\gamma = \frac{1 + N_B \cdot D_E \cdot X_B}{(N_E \cdot D_B \cdot X_E)}^{-1}$$

$$= \frac{1 + 5E16 \cdot 1.5 \cdot 0.8E-4}{(1E19 \cdot 23.94 \cdot 0.8E-4)}^{-1}$$

$$\gamma = 0.99969$$

a [7] $\gamma =$ _____ .

b Calculate the base transport factor for this device.

$$\tau_{U\min B} = \frac{45E-6}{1 + 5E16/1E17 + (5E16/5E17)^2} = 2.980E-05 \text{ sec}$$

$$L_{\min B} = \sqrt{D_{b\min} \cdot \tau_{U\min B}} = 2.671E-02 \text{ cm}$$

$$\alpha_T = \frac{1 + X_B^2 / (2 \cdot L_{\min B}^2)}{2}^{-1} = 0.9999955$$

b [6] $\alpha_T =$ _____ .

c Calculate the IS parameter for this device.

$$I_S = q \cdot n_i^2 \cdot A \cdot D_b / (N_b \cdot X_B) = q \cdot n_i^2 \cdot 100E-8 \cdot 23.94 / (5E16 \cdot 0.8E-4)$$

$$I_S = 2.016E-16 \text{ A}$$

c [6] $I_S =$ _____ A.

d Calculate the β_F parameter for this device.

$$\alpha_F = \gamma \cdot \alpha_T \cdot \delta, \text{ assume } \delta = 1$$

$$\beta_F = \frac{\alpha_F}{1 - \alpha_F} = \frac{\gamma \cdot \alpha_T}{1 - \gamma \cdot \alpha_T}$$

$$\beta_F = \frac{0.99969 \cdot 0.9999955}{1 - 0.99969 \cdot 0.9999955} = 3179$$

d [6] $\beta_F =$ _____ .