

Test 01-EE 5342/051 (print last name) _____ (print first name) **KEY** _____
 Tuesday, February 18, 2003, 9:30 AM, 106 Nedderman Hall
75 minutes allowed (last four digits of your student #) _____ (e-mail if new) _____

Instructions:

- Do your own work. DO NOT REMOVE THE STAPLE ON THIS EXAM.
- You may use either a legal copy of the text OR ONE sheet of hand-written notes. You may NOT pass a book or note sheet to another student. You may NOT use class notes, nor previously solved problems.
- Calculator allowed. You may NOT share a calculator with another student.
- Use values given on this cover sheet. If a value is not given, explicitly state definitions and assumptions that you use.
- Where possible, calculate parameters rather than read them from a graph.
- Do all work in the spaces provided on this exam paper. If you write on the back of a sheet, make the notation "PTO" in your solution in order to assure that material written on the back of the page is evaluated for a grade. AN EXTRA BLANK SHEET IS ATTACHED AT THE BACK OF THE EXAM.
- Show all calculations, making numerical substitutions and giving numerical results where possible.
- Write answers in space given.
- Unless stated otherwise,
 $T = 300\text{K}$, $V_t = 25.852\text{ mV}$
- Unless otherwise stated, the material is silicon with
 $n_i = 1.07\text{E}10\text{ cm}^{-3}$ $N_c = 2.84\text{E}19\text{ cm}^{-3}$ $q\chi_{\text{Si}} = 4.05\text{ eV}$
 $E_{g,\text{Si}} = 1.125\text{ eV}$ $N_v = 3.08\text{E}19\text{ cm}^{-3}$
- For the work function of poly silicon, use
 $\phi_{n+} = \chi_{\text{Si}} = 4.05\text{ V}$
 $\phi_{p+} = \chi_{\text{Si}} + E_{g,\text{Si}}/q = 5.175\text{ V}$.
- For minority carrier (either electrons or holes) lifetime in silicon, use the relationship
 $\tau_{\text{min}} = (45\text{E}-6\text{ sec}) / (1 + 7.7\text{E}-18 * N_{\text{imp}} + 4.5\text{E}-36 * N_{\text{imp}}^2)$,
 where N_i = the total impurity concentration
- For holes in silicon, assume
 $\mu_p = \{418.3 \div [1 + (N_i \div 1.6\text{E}17)^{0.7}] + 49.7$, in $\text{cm}^2/\text{V-sec}$
 (where N_i = the total impurity concentration in n- or p-type material, compensated or not).
- For electrons in silicon, assume
 $\mu_n = \{1268 \div [1 + (N_i \div 1.3\text{E}17)^{0.91}] + 92$, in $\text{cm}^2/\text{V-sec}$
 (where N_i = the total impurity concentration in n- or p-type material, compensated or not).
- Metal gate work functions should be assumed to be
 $\phi_{\text{Au}} = 4.75\text{ V}$ for gold, $\phi_{\text{Al}} = 4.1\text{V}$ for aluminum.
- The electron affinity of SiO_2 is $\chi_{\text{SiO}_2} = 0.95\text{ V}$.
- Planck constant $h = 6.62618 \times 10^{-34}\text{ J-s} = 4.1354 \times 10^{-15}\text{ eV-s}$, $1\text{ eV} = 1.60218 \times 10^{-19}\text{ Joule}$
- free electron mass $m_o = 9.1095 \times 10^{-28}\text{ g}$.
- Boltzmann constant, $k = 1.38066 \times 10^{-23}\text{ J/K}$
- Electron charge, $q = 1.60218 \times 10^{-19}\text{ Coulomb}$
- Permittivity of free space, $\epsilon_o = 8.85418 \times 10^{-14}\text{ Fd/cm}$
- Relative permittivity of silicon, $\epsilon_r = 11.7$
- Relative permittivity of silicon dioxide, $\epsilon_{\text{rOx}} = 3.9$
- The breakdown voltage of an abrupt (step) junction (assymetrical or one-sided) diode with doping on the lightly doped side of N_B is $V_B = 60(E_g/1.1)^{3/2} (10^{16}/N_B)^{3/4}\text{ V}$.
- Each part is worth [x] points, as given in the problem.
- In each case, make the simplest realistic computation, noting the assumptions

A p+n junction with $N_a=1e18/cm^3$ and $N_d=5E15/cm^3$ on the p+ side and $N_d=5E15/cm^3$ on the n side is fabricated with a junction area $A_j = 235E-8 cm^2$. It is known that the charge neutral region width of the lightly doped side (at $v_d = 0$) is 1 micron.

1. Find τ_{AU} on the p-side

$$\tau_{AUmin} = (45E-6 \text{ sec}) / (1 + 7.7E-18 * N_{imp} + 4.5E-36 * N_{imp}^2), N_{imp} = 1e18 + 5e15$$

$$\tau_{AU} = 3.39E-06 \text{ sec}$$

Answer 1[10]. _____

2. Find D_p on the n-side

$$D_p = \mu_p * V_t = V_t * ((418.3 / (1 + (N_i / 1.6E17)^{0.7})) + 49.7), N_i = N_a = 5e15$$

$$D_p = V_t * ((418.3 / (1 + (5e15 / 1.6E17)^{0.7})) + 49.7)$$

$$D_p = 11.2 \text{ cm}^2/\text{sec}$$

Answer 2[10]. _____

3. Evaluate the IS for this diode.

Assume "short diode", which is appropriate since $W_{cnr} = 1 \text{ micron}$.

$$IS = q * n_i^2 * A * D_p / (N_d * W_{cnr}) = q * n_i^2 * 235e-8 * 11.2 / (5e15 * 1e-4)$$

$$IS = 9.67E-16 \text{ Amp}$$

Answer3[10]. _____

4. Using the procedure of Massobrio and Antognetti, estimate I_{KF} , the current at which the high level injection diode current is the same as the low level injection diode current.

$$I_{KFlli} = q * n_i^2 * A * D_p / (N_d * W_{cnr}) \exp(V_{KF} / V_t) \text{ low-level injection at } V_{KF}$$

$$I_{KFhli} = 2 * q * n_i * A * D_p / W_{cnr} * \exp(V_{KF} / (2 * V_t)) \text{ high-level injection at } V_{KF}, \text{ M\&A eqn. 1-14}$$

equating $I_{KFlli} = I_{KFhli}$, and solving for V_{KF} , then substituting into I_{KFlli} ,

$$V_{KF} = 2 * V_t * \ln(2 * N_d / n_i) = 0.711 \text{ V}$$

$$I_{KF} = IS * \exp(2 * \ln(2 * N_d / n_i)) = 8.45E-04 \text{ A}$$

Answer 4[10]. _____

5 [6]. Why does the high level injection current formula given in Massobio and Antognetti differ from that given in the lectures by a factor of 2?

In class, we neglected the majority carrier diffusion in the high level injection calculation.

6. Evaluate $V_{bi} = V_J$ for this diode.

$$V_{bi} = V_t \ln\left(\frac{N_a - N_d}{n_i} \frac{N_d}{n_i}\right) = V_t \ln\left(\frac{1e18 - 5e15}{n_i} \frac{5e15}{n_i}\right)$$
$$V_{bi} = 0.812 \text{ V}$$

Answer 6[10]. _____

7. Evaluate CJO for this diode.

$$CJO = \epsilon_0 \epsilon_{Si} \frac{A}{W(0)} = A \sqrt{\epsilon_0 \epsilon_{Si} q N_d / (2 V_{bi})}$$
$$CJO = 5.31288E-14 \text{ Fd}$$

Answer 7[10]. _____

8. Evaluate the diffusion conductance at $v_d = v_j = 650 \text{ mV}$ for this diode (assume $N = 1$, and there is negligible recombination current).

$$g_d = i_D / (N V_t) = I_S \exp(0.650 / V_t) / V_t$$
$$g_d = 3.11E-03 \text{ A/V}$$

Answer 8[10]. _____

9. Evaluate the diffusion capacitance at $v_d = v_j = 650$ mV for this diode (assume $N = 1$, and there is negligible recombination current).

$$C_d/g_d = \tau_{Utr} = Wpcnr^2/(2D_p)$$

$$\tau_{Utr} = 4.45611E-10$$

$$C_d = Wpcnr^2 * g_d / (2 * D_p)$$

$$C_d = 1.39E-12 \quad F_d$$

Answer 9[10]. _____

10. A different diode has $i_d = 100$ microAmp at $v_d = 600$ mV and $i_d = 125.89$ microAmp at $v_d = 606$ mV. Find the effective ideality factor N_{eff} for this diode.

$$d(\ln(i_d))/dv_d = (N_{eff} * V_t)^{-1}$$

$$N_{eff} = (d(\ln(i_d))/dv_d * V_t)^{-1} = (v_2 - v_1) / (\ln(i_2/i_1) * V_t)$$

$$N_{eff} = 1.008$$

Answer 10[14]. _____