UTA PhD Diagnosis Exam (Fall 2012)

Power Electronics

Instructions:
- Verify that your exam contains 7 pages (including the cover sheet).
- Please be sure to use blank paper to write your answers. If more space is needed, please ask the instructor for extra paper. DO NOT WRITE ON THE BACK OF A SHEET!
- The point values listed on this exam serve only as a guideline. The Dept reserves the right to make modifications to the weighting of the problems.
- Calculator is okay.

I Choose to work on Problems _____ and _______ (Choose only 2 from the 3 problems).

<table>
<thead>
<tr>
<th>Problem</th>
<th>Possible Points</th>
<th>Scores</th>
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<tbody>
<tr>
<td>1</td>
<td>50</td>
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<td>2</td>
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<td>3</td>
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<td>Total Score (Choose 2 Problems)</td>
<td>100</td>
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1. Find the steady-state voltage conversion ratio (output voltage/input voltage), as the function of the duty cycle, for the both circuits shown (25 points each). Both converters are in continues conduction mode, and the duty cycle is applied to the switch number 1:

Converter 1:

Converter 2:
2. A buck converter is designed with a 5 V input, 0.25 ohm load, and 100 kHz switching frequency. The input voltage varies between 30 V and 60 V. The load power ranges between 10 and 200 W. What would be the minimum inductance that ensures operation in the continuous conduction mode, $L_{\text{min}}$ (25 points)? What would be the maximum inductance that ensures operation in the discontinuous conduction mode, $L_{\text{max}}$ (25 points)?
3. The following ideal boost converter is in discontinuous conduction mode, with the inductor current waveform shown on the right. The switching frequency is $f_{sw}$.

a) Find the duty ratio constraint, $d_2$, as the function of $\bar{i}_L$ (25 points).

b) Find the full-order state-space averaged model of the converter in discontinuous conduction mode (25 points).